



# VIGNAN'S INSTITUTE OF MANAGEMENT AND TECHNOLOGY FOR WOMEN

Sponsored by Lavu Educational Society, Approved by AICTE, New Delhi & Affiliated to JNTUH, Hyderabad.

Kondapur (V), Ghatkesar (M), Medchal - Malkajgiri (D) - 501 301 TS INDIA

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Email: info@vmtw.in

## Department of Computer Science and Engineering

### II Year-II Semester

<b>COURSE: Computer Organization _CS401BS</b>	
<b>Subject Code</b>	<b>Course Outcomes</b>
<b>CS401BS.1</b>	Understand the basic operations of digital computer system to learn the integrated circuit logic families and its microoperation
<b>CS401BS.2</b>	Analyze the machine instructions and codes to implement or translate the sequence of microoperation
<b>CS401BS.3</b>	Apply the fundamental issues related to computer arithmetic operation and circuits to support the system computation
<b>CS401BS.4</b>	Understand the various components of memory system to organize the operational units of CPU
<b>CS401BS.5</b>	Analyze the data processing operations of central processing and control unit to design the CPU specification and Understand the concepts of micro processor and programming

### Mapping

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	CO Average
<b>CS401BS.1</b>	3	2	2	2	1	1	1	1	0	1	0	1	1.25
<b>CS401BS.2</b>	2	3	2	2	1	1	1	1	0	1	0	1	1.25
<b>CS401BS.3</b>	3	2	2	2	2	2	1	1	0	1	0	1	1.42
<b>CS401BS.4</b>	3	2	2	2	2	2	1	1	0	1	0	1	1.42
<b>CS401BS.5</b>	3	2	1	2	2	2	1	1	0	1	0	1	1.33
<b>PO Average</b>	2.75	2.25	2	2	1.5	1.5	1	1	0	1	0	1	1.33



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### II Year-II Semester

<b>COURSE: Database Management Systems _CS402ES</b>	
<b>Subject Code</b>	<b>Course Outcomes</b>
<b>CS402ES.1</b>	Understand data models to design a database
<b>CS402ES.2</b>	Illustrate the conceptual design for Large enterprises
<b>CS402ES.3</b>	Formulate SQL queries and integrity constraints over relations
<b>CS402ES.4</b>	Apply normalization on database for eliminating redundancy
<b>CS402ES.5</b>	Summarize transaction properties, concurrency control and recovery techniques AND Explain various data storage and security mechanisms

### Mapping

<b>CO</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>PO9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO12</b>	<b>CO Average</b>
<b>CS402ES.1</b>	3	3	2	2	2	1	1	1	2	2	0	1	1.67
<b>CS402ES.2</b>	3	3	2	2	2	1	1	1	2	2	0	1	1.67
<b>CS402ES.3</b>	3	3	2	2	2	1	1	1	2	2	0	1	1.67
<b>CS402ES.4</b>	3	3	3	3	3	1	1	1	3	3	0	1	2.08
<b>CS402ES.5</b>	2	2	3	3	3	0	0	0	3	3	0	0	1.58
<b>PO Average</b>	2.8	2.8	2.4	2.4	2.4	0.8	0.8	0.8	2.4	2.4	0	0.8	1.74



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## Department of Computer Science and Engineering

### II Year-II Semester

<b>COURSE: Operating Systems _CS403ES</b>	
<b>Subject Code</b>	<b>Course Outcomes</b>
<b>CS403ES.1</b>	Explain the concepts of process and implement CPU scheduling algorithms
<b>CS403ES.2</b>	Illustrate different problems and solutions related to process synchronization
<b>CS403ES.3</b>	Understand deadlock and use various algorithms to handle deadlocks
<b>CS403ES.4</b>	Understand various memory management mechanisms
<b>CS403ES.5</b>	Analyze various mechanisms used in virtual memory management and Understand the goals and principles of protection

### Mapping

<b>CO</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>PO9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO12</b>	<b>CO Average</b>
<b>CS403ES.1</b>	3	3	2	2	2	1	1	1	2	2	0	1	1.67
<b>CS403ES.2</b>	3	3	2	2	2	1	1	1	2	2	0	1	1.67
<b>CS403ES.3</b>	3	3	2	2	2	1	1	1	2	2	0	1	1.67
<b>CS403ES.4</b>	3	3	3	3	3	1	1	1	3	3	0	1	2.08
<b>CS403ES.5</b>	2	2	3	3	3	0	0	0	3	3	0	0	1.58
<b>PO Average</b>	2.8	2.8	2.4	2.4	2.4	0.8	0.8	0.8	2.4	2.4	0	0.8	1.74



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## Department of Computer Science and Engineering

### II Year-II Semester

<b>COURSE: Formal Language and Automata Theory _CS404ES</b>	
<b>Subject Code</b>	<b>Course Outcomes</b>
<b>CS404ES.1</b>	Understand finite automata and regular expressions
<b>CS404ES.2</b>	Demonstrate the minimization of DFA, Conversion of NFA to DFA
<b>CS404ES.3</b>	Summarize context free grammar and construction of PDA
<b>CS404ES.4</b>	Understand Chomsky classification and design Turing machine
<b>CS404ES.5</b>	Apply working skills in theory and application of finite state machines in compiler design and Summarize computability, decidability and complexity through problem solving

### Mapping

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	CO Average
CS404ES.1	1	2	2	2	2	2	1	1	1	1	0	1	1.33
CS404ES.2	1	2	2	2	2	1	1	1	1	1	0	1	1.25
CS404ES.3	2	2	2	2	2	2	1	1	1	1	0	1	1.42
CS404ES.4	2	2	2	2	2	1	1	1	1	1	0	1	1.33
CS404ES.5	2	2	2	2	2	2	1	1	1	1	0	1	1.42
<b>PO Average</b>	1.6	2	2	2	2	1.6	1	1	1	1	0	1	1.35



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## Department of Computer Science and Engineering

### II Year-II Semester

<b>COURSE: Business Economics and Financial Analysis _SM405MS</b>	
<b>Subject Code</b>	<b>Course Outcomes</b>
<b>SM405MS.1</b>	Understand the basic Business types, impact of the Economy on Business and Firms specifically.
<b>SM405MS.2</b>	Analyze the demand for a product or product mix of a company and compute the future sales level of a product by using various quantitative and qualitative techniques and with the help of past sales data.
<b>SM405MS.3</b>	Apply the concepts of cost behaviour for managerial decision making and determine Break Even Point (BEP) of an enterprise.
<b>SM405MS.4</b>	Analyze the Business from the Financial Perspective.
<b>SM405MS.5</b>	Examine the steps, relate the concepts of Pay Back, ARR, NPV, PI and IRR methods of Capital Budgeting and compute rank of the projects. Analyze, interpret and comment on the financial statements of a business enterprise by using liquidity leverage, coverage and turnover and profitability ratios..

### Mapping

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	CO Average
<b>SM405MS.1</b>	0	1	0	1	1	2	0	3	2	2	2	0	1.17
<b>SM405MS.2</b>	1	1	2	2	1	0	0	0	0	0	1	0	0.67
<b>SM405MS.3</b>	1	1	2	2	1	0	0	0	0	0	3	0	0.83
<b>SM405MS.4</b>	1	1	2	2	1	0	0	0	0	0	3	0	0.83
<b>SM405MS.5</b>	0	1	0	0	2	0	0	0	2	2	3	0	0.83
<b>PO Average</b>	0.6	1	1.2	1.4	1.2	0.4	0	0.6	0.8	0.8	2.4	0	0.87



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### II Year-II Semester

<b>COURSE: Computer Organization Lab _ CS406ES</b>	
<b>Subject Code</b>	<b>Course Outcomes</b>
<b>CS406ES.1</b>	Describe and explain the operation of fundamental digital gates .
<b>CS406ES.2</b>	Analyze the operation of medium complexity standard combinational circuits ,a flip0flop, counters and shift registers and examine relevant timing diagrams .
<b>CS406ES.3</b>	Design and operate practical digital logic circuits and Report findings and evaluate results
<b>CS406ES.4</b>	Understand knowledge of the 8086 instruction set and utilize it in programming and Perform Logical, Arithmetic, and Rotate/Shift operations on Data
<b>CS406ES.5</b>	Implement sorting and searching an array and string manipulation for 8086

### Mapping

<b>CO</b>	<b>PO1</b>	<b>PO2</b>	<b>PO3</b>	<b>PO4</b>	<b>PO5</b>	<b>PO6</b>	<b>PO7</b>	<b>PO8</b>	<b>PO9</b>	<b>PO10</b>	<b>PO11</b>	<b>PO12</b>	<b>CO Average</b>
<b>CS406ES.1</b>	3	2	2	2	1	1	1	1	0	1	0	1	1.25
<b>CS406ES.2</b>	2	3	2	2	1	1	1	1	0	1	0	1	1.25
<b>CS406ES.3</b>	3	2	2	2	2	2	1	1	0	1	0	1	1.42
<b>CS406ES.4</b>	3	2	2	2	2	2	1	1	0	1	0	1	1.42
<b>CS406ES.5</b>	3	2	1	2	2	2	1	1	0	1	0	1	1.33
<b>PO Average</b>	2.8	2.2	1.8	2	1.6	1.6	1	1	0	1	0	1	1.33



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### II Year-II Semester

<b>COURSE: Database Management Systems Lab _ CS407ES</b>	
<b>Subject Code</b>	<b>Course Outcomes</b>
<b>CS407ES .1</b>	Understand basic probability axioms and apply Bayes' theorem related to engineering problems.
<b>CS407ES .2</b>	Identify the suitable distribution among Binomial, Poisson, exponential, normal in engineering applications.
<b>CS407ES .3</b>	Make use of the sampling distribution of the sample mean in general situations, using the Central Limit Theorem.
<b>CS407ES .4</b>	Decide the null or alternative hypotheses using the suitable test statistic.
<b>CS407ES .5</b>	Apply the regression analysis to fit the curves. Understand the methods and applications of queuing models in engineering

### Mapping

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	CO Average
CS407ES .1	3	3	2	2	2	1	1	1	2	2	0	1	1.67
CS407ES .2	3	3	2	2	2	1	1	1	2	2	0	1	1.67
CS407ES .3	3	3	2	2	2	1	1	1	2	2	0	1	1.67
CS407ES .4	3	3	3	3	3	1	1	1	3	3	0	1	2.08
CS407ES .5	2	2	3	3	3	0	0	0	3	3	0	0	1.58
PO Average	2.8	2.8	2.4	2.4	2.4	0.8	0.8	0.8	2.4	2.4	0	0.8	1.74



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## Department of Computer Science and Engineering

### II Year-II Semester

COURSE: Operating Systems Lab _CS408ES	
Subject Code	Course Outcomes
CS408ES.1	Implementing CPU scheduling Algorithms
CS408ES.2	Implementing File Allocation Strategies
CS408ES.3	Implementing File Organization Techniques
CS408ES.4	Implementing Bankers Algorithm for Deadlock Avoidance and Prevention
CS408ES.5	Implementing Page Replacement Algorithms and Paging Techniques for Memory Allocation

### Mapping

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	CO Average
CS408ES.1	3	3	2	2	2	1	1	1	2	2	0	1	1.67
CS408ES.2	3	3	2	2	2	1	1	1	2	2	0	1	1.67
CS408ES.3	3	3	2	2	2	1	1	1	2	2	0	1	1.67
CS408ES.4	3	3	3	3	3	1	1	1	3	3	0	1	2.08
CS408ES.5	2	2	3	3	3	0	0	0	3	3	0	0	1.58
PO Average	2.8	2.8	2.4	2.4	2.4	0.8	0.8	0.8	2.4	2.4	0	0.8	1.73



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## Department of Computer Science and Engineering

### II Year-II Semester

COURSE: Gender Sensitization Lab _ MC400HS	
Subject Code	Course Outcomes
MC400HS.1	Students will have developed a better understanding of important issues related to gender in contemporary India.
MC400HS.2	Students will be sensitized to basic dimensions of the biological, sociological, psychological and legal aspects of gender. This will be achieved through discussion of materials derived from research, facts, everyday life, literature and film
MC400HS.3	Students will attain a finer grasp of how gender discrimination works in our society and how to counter it..
MC400HS.4	Students will acquire insight into the gendered division of labour and its relation to politics and economics.
MC400HS.5	Students will develop a sense of appreciation of women in all walks of life. Men and women students and professionals will be better equipped to work and live together as equals.

### Mapping

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	CO Average
MC400HS.1	0	1	0	1	1	2	3	3	2	3	3	3	1.83
MC400HS.2	0	1	0	1	1	2	1	2	1	2	2	1	1.17
MC400HS.3	3	1	1	1	0	3	1	3	3	3	3	3	2.08
MC400HS.4	0	0	1	0	2	1	1	2	2	3	2	2	1.33
MC400HS.5	0	0	0	1	1	1	1	1	0	1	1	1	0.67
PO Average	0.6	0.6	0.4	0.8	1	1.8	1.4	2.2	1.6	2.4	2.2	2	1.42